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Study on the effect of the substrate material type and thickness on the performance of the filtering antenna design

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Article Info	ABSTRACT
	This article presents a new design of a four-pole microstrip filtering antenna. The filtering antenna consists of a bandpass filter, which has four resonators integrated to a monopole patch antenna. The filtering antenna is designed with a relatively high bandwidth of about 1.22 GHz to satisfy a high-speed data transmission. Three types of dielectric substrate materials were used for the design of the filtering antenna, which is RT/Duroid 5880, RO3003, and FR-4. The simulation results of the filtering antenna design, which are established on the three different dielectric substrate materials, are done by using Computer Simulation Technology (CST) software. Comparison results of the filtering antenna that is established on the three different dielectric substrate materials are done at a fixed substrate height and different substrate heights. The filtering antenna is designed at a center frequency f0 = 2.412 GHz, which is suitable for WLAN applications. *This is an open access article under the CC BY-SA license.*

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1. INTRODUCTION

Ever-increasing demand for compact wireless communication transceivers continues to impact the field of microwave (MW) and radio frequency (RF) applications [1, 2]. Some of the most important modules in such systems are the microstrip antennas and filters [3-15], and its performance greatly effects the performance of the whole wireless communication systems. In recent years, the microstrip filtering antenna has become one of the best-desired device structures because of their low profile, compact size, lightweight, and ease of fabrication. The microstrip filtering antenna is beneficial because it can be directly printed onto the dielectric substrate materials [16]. Filtering antenna design has many applications, mostly in modern wireless communication systems because of the filtering and radiating responses are occurring simultaneously [17].

It is known that the use of the substrate material in the design of RF/microwave circuits is a common and essential issue. One of the design basics is to choose the appropriate substrate material type as well as thickness to fit the proposed application. Finding the dielectric substrate for printed-circuit-board (PCB) materials provides a high-performance amount for acceptable charges at these frequencies represent a great challenge. By recognizing the key parameters and features of interest to PCB materials at high

frequencies, such as how different types of PCB's behave for different types of substrate materials at high frequencies (millimeter-wave frequencies). The selection can be carefully conceived when choosing printed circuit board materials for use in high frequencies [18]. This work presents three different types of dielectric substrate materials to be used in filtering antenna design and then check the design quality and its suitability for the application, which is designed for.

2. PROPERTIES OF THE DIELECTRIC SUBSTRATES
2.1. FR-4 glass epoxy dielectric substrate
FR-4 is a low-cost PCB material, made from fiberglass textile implemented in epoxy resin. The "FR" in FR-4 indicates to "fire resistant". It has mostly substituted the (flammable) board material G-10 because of this feature. The FR-4 material, usually works well, when designing below 1 GHz. However, as frequencies rise beyond 1 GHz, the passive circuit elements have to be taken into consideration. The main considerations for circuit design in the 3-6 GHz involve skin effect, surface roughness, proximity effect, and dielectric substrate [19]. The FR-4 dielectric constant ε_r has been reported between 4.3–4.8, and is slightly dependent on the frequency. The loss tangent $\tan\delta$ of FR-4 is 0.018.

2.2. RT/Duroid 5880 dielectric substrate
Composites RT/Duroid 5880 microfiber reinforced PTFE is designed for demanding stripline and microstrip line applications. RT/Duroid is a glass microfiber reinforced PTFE (Poly Tetra Fluoro Ethylene) composite built by Roger Corporation. They show a very good chemical resistance, involving solvent and reagents utilized in printing and coating, ease of cutting and fabrication, shearing, machining, and environment-friendly [7]. RT/Duroid 5880 has a low loss tangent $\tan\delta$ of about 0.004 and dielectric constant $\varepsilon_r = 2.2$ [20].

2.3. RO3003 dielectric substrate
Title RO3003 laminates is a ceramic-filled (Poly Tetra Fluoro Ethylene) PTFE composite circuit material with mechanical characteristics, which are uniform regardless of the choosing of the dielectric constant. This case permits the designers to develop multi-layer board designs, which are used differently dielectric constant materials for individual layers, without facing accuracy problems [21]. RO3003 has low loss tangent $\tan\delta = 0.0013$ and dielectric constant $\varepsilon_r = 3.0$.

3. FILTERING ANTENNA STRUCTURE
The layout of the proposed filtering antenna structure is shown in Figure 1. The filtering antenna consists of four pole bandpass filter and monopole patch antenna. The integration of the BPF with monopole patch antenna is done by connecting the second port of the BPF with the antenna. A 50-Ω microstrip feed-line was fed both the BPF and monopole patch antenna, so there is no need to the additional matching circuit. The four-pole bandpass filter consists of four resonators, which are connected to the microstrip feed-line established on a dielectric substrate material with ground plane has an L-shaped slot etched as shown in Figure 2. Each resonator consists of the square open loop with a longitudinal strip ended with E-shaped arms.
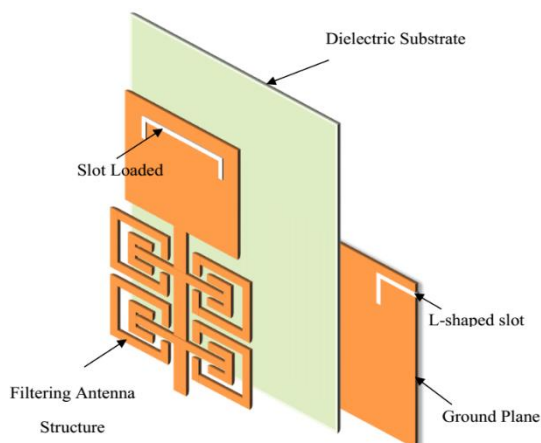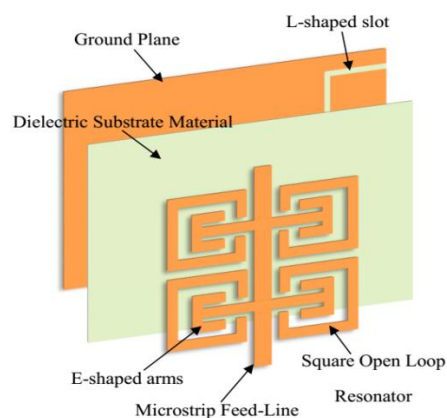

Figure 1. Filtering antenna structure layout

Figure 2. Four-pole bandpass filter structure layout

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The filtering antenna was established on three different types of dielectric substrate materials, which are FR-4, RT/Duroid 5880, and RO3003. Firstly, these substrate materials are kept at a fixed thickness, and then the thickness of each dielectric substrate was changed to investigate, which of these three types are more suitable for the specifications for this application. The absorption of electrical energy by a dielectric material, which is exposed to an alternating electric field, is named dielectric loss. Dielectric loss is due to the influence of the limited loss tangent, tanδ in which the losses increasing and directly proportional to the operating frequency. Generally, the dielectric constant of the dielectric substrate ε_r is a complex number and is given by:

$$\varepsilon_r = \varepsilon_r' + j\varepsilon_r''$$ (1)

where,
ε_r' - Is a real part of the dielectric constant.
ε_r'' – Is an imaginary part of the dielectric constant.
Then the loss tangent is given as [22]:

$$tan\delta = \frac{\varepsilon_r''}{\varepsilon_r'}$$ (2)

the relationship between the tangent loss and dielectric loss is given by the following formula [23]:

$$\alpha_d = \frac{\varepsilon_r}{\sqrt{\varepsilon_{reff}}} \cdot \frac{\varepsilon_{reff}-1}{\varepsilon_r-1} \cdot \frac{\pi}{\lambda_o} \cdot tan\delta_d$$ (3)

where,
α_d – Is a dielectric loss.
ε_{reff} – Is the effective dielectric constant of the substrate material.
λ_o – Is a free space wavelength.
Figure 3, shows the dielectric loss for the three types of dielectric substrates, which are used in this research article. The microstrip propagation delay t_{pd} is a function of a substrate dielectric constant ε_r:

$$t_{pd}(ns/ft) = 1.017\sqrt{0.475\varepsilon_r + 0.67}$$ (4)

Attenuation sources of practical microstrip lines are due to the effects of radiation and finite conductivity of the conductor lines. In addition, energy in a microstrip is depending on the dielectric constant ε_r, substrate thickness h, and the circuit geometry. Using low dielectric constant substrate has a low concentration energy, which leads to a great radiation losses. Figure 4, shows the relationship of the propagation delay with respect to the dielectric constant of different types of dielectric substrate materials.
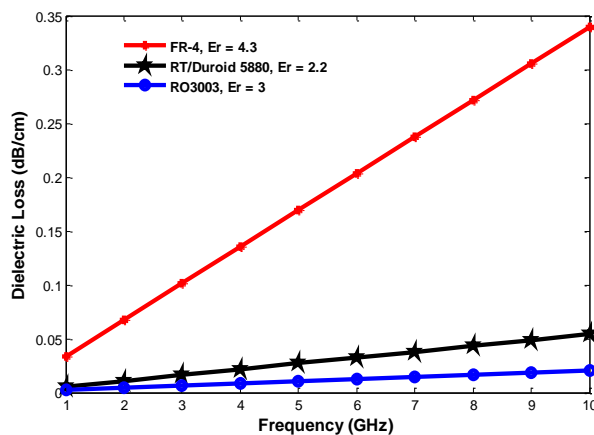

Figure 3. Dielectric loss versus frequency for different types of dielectric substrate materials
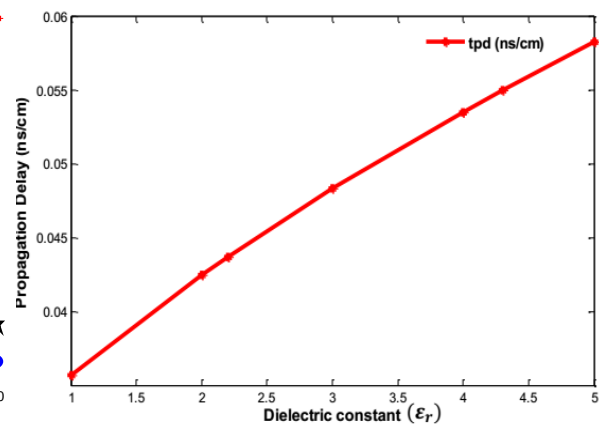
Figure 4. Propagation delay versus dielectric constant

4. RESULTS AND DISCUSSION

Figure 5, shows the filtering antenna design structure with its optimized dimensions, and the optimized dimensions are stated in Table 1. Figure 6, shows the reflection parameter (S_{11}-parameter) and gain for the filtering antenna, which is designed on RT/Duroid 5880 dielectric substrate with height h = 30.944 mil and dielectric constant 2.2. The filtering antenna has designed at a center frequency f_o = 2.412 GHz, and a band edges f_1 = 1.872 GHz and f_2 = 3.1099 GHz. The filtering antenna is suitable for WLAN applications and a relatively high bandwidth is fit the fast data transmission, which is required in a modern communication systems. Figure 7, shows the S_{11}-parameter of the filtering antenna without L-shaped Defected Ground Structure (DGS).
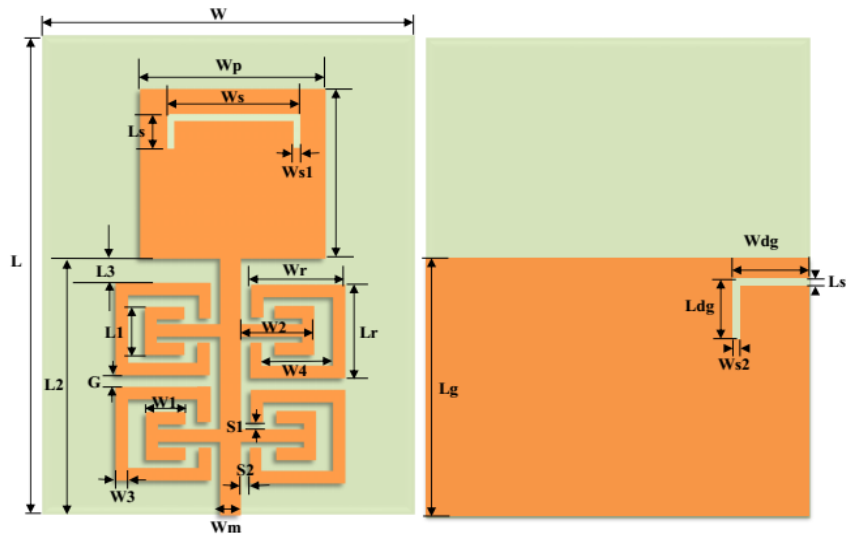

Figure 5. Filtering antenna Structure with its optimized dimensions

Table 1. The optimized dimension of the filtering antenna

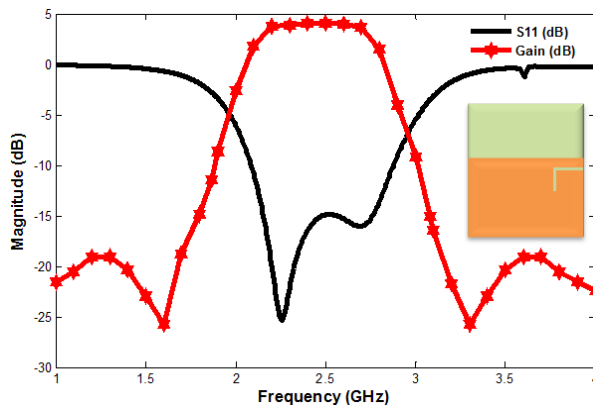| Parameter	W	W_P	W_S	W_{S1}	W_1
Dimension (mm)	45	20	15	0.5	3.9
Parameter	W_4	W_m	W_r	W_{dg}	W_{S2}
Dimension (mm)	8	2.4	10	7.5	0.5
Parameter	L_g	L_r	L_{S1}	L_1	L_2
Dimension (mm)	25.4	9	0.4	4	26
Parameter	W_2	W_3	S_1	L	L_p
Dimension (mm)	6.6	1	0.5	48	18
Parameter	L_{dg}	L_3	S_2	G	L_s
Dimension (mm)	5.4	3	0.8	1	3.5

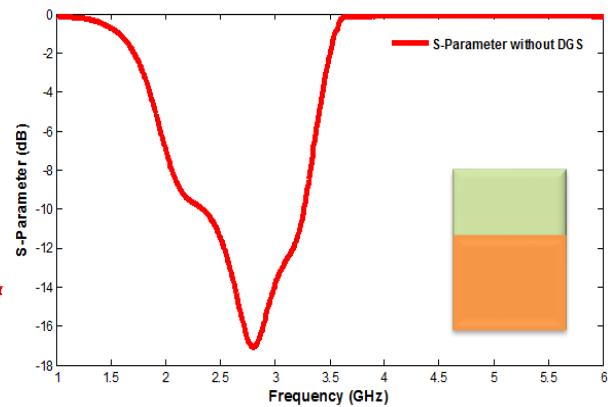Figure 6. S_{11}-parameter and gain of the filtering antenna

Figure 7. S_{11}-parameter of the filtering antenna without DGS

Etching the DGS in the ground plane of the filtering antenna disturbed the shield current distribution in a wave-guiding structure. This disturbance will change properties of the design such as the effective capacitance and effective inductance [24]. The feature of DGS is a slow-wave impact because the equivalent LC components that may decrease the design circuit size [25]. From Figures 6 and 7, can notice that the significant and obvious effect of the DGS on the overall performance of the filtering antenna response. Figure 8, shows the S_{11}-parameter of the filtering antenna without the Square Open Loop Resonator (SOLR).
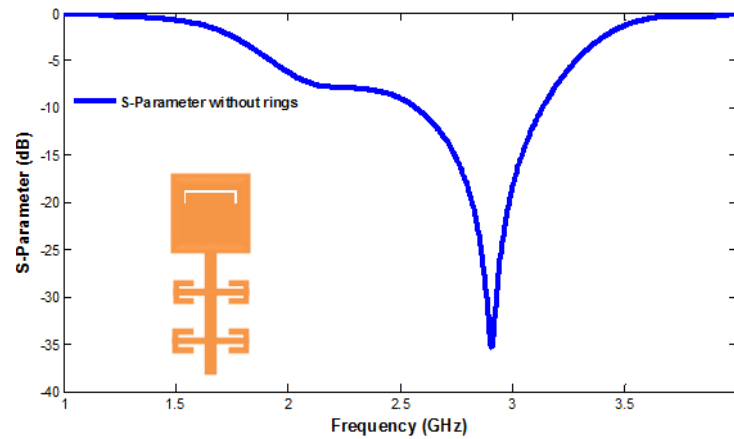

Figure 8. S_{11}-parameter of the filtering antenna without SOLR

Figure 9, shows the Far-field radiation pattern of the filtering antenna at a center frequency f_o = 2.412 GHz. The simulation results are obtained by using Computer Simulation Technology (CST) software. The comparison graph of the reflection parameter for the three types of dielectric substrate material at a fixed substrate height h = 30.944 mils is shown in Figure 10. The effect of the dielectric substrate material on the design performance especially the center frequency and reflection parameter. Table 2 contains the comparison of some parameters of the three different dielectric substrate types for which the filtering antenna is designed and established in.

Figure 11, shows the comparison of the reflection parameter of the three different dielectric substrate materials, which the filtering antenna design is, established in, for different substrate heights. This comparison is a necessary issue to illustrate the effect of the dielectric substrate material type, and thickness. Table 3 contains the comparison of some of the important parameters involved in the design of the filtering antenna circuit on the three different dielectric substrate material types. The parameter comparison of these three types of the dielectric materials is stated in Table 3.

Table 2. Comparison of some different parameters for the different dielectric substrate materials

Parameters	Dielectric Substrate type		
	RT/ 5880	RO3003	FR-4
Center frequency (f_o GHz)	2.412	2.202	1.924
Return loss RL (dB)	-15	-12.065	-6.0314
Maximum Gain (dB)	4.03	2.43	1.18
BW (GHz)	1.22	0.922	0.657
VSWR	1.1937	1.58	2.2

Table 3. The comparison of some of the important parameters involved in the design of the filtering antenna circuit on the three different dielectric substrate material types

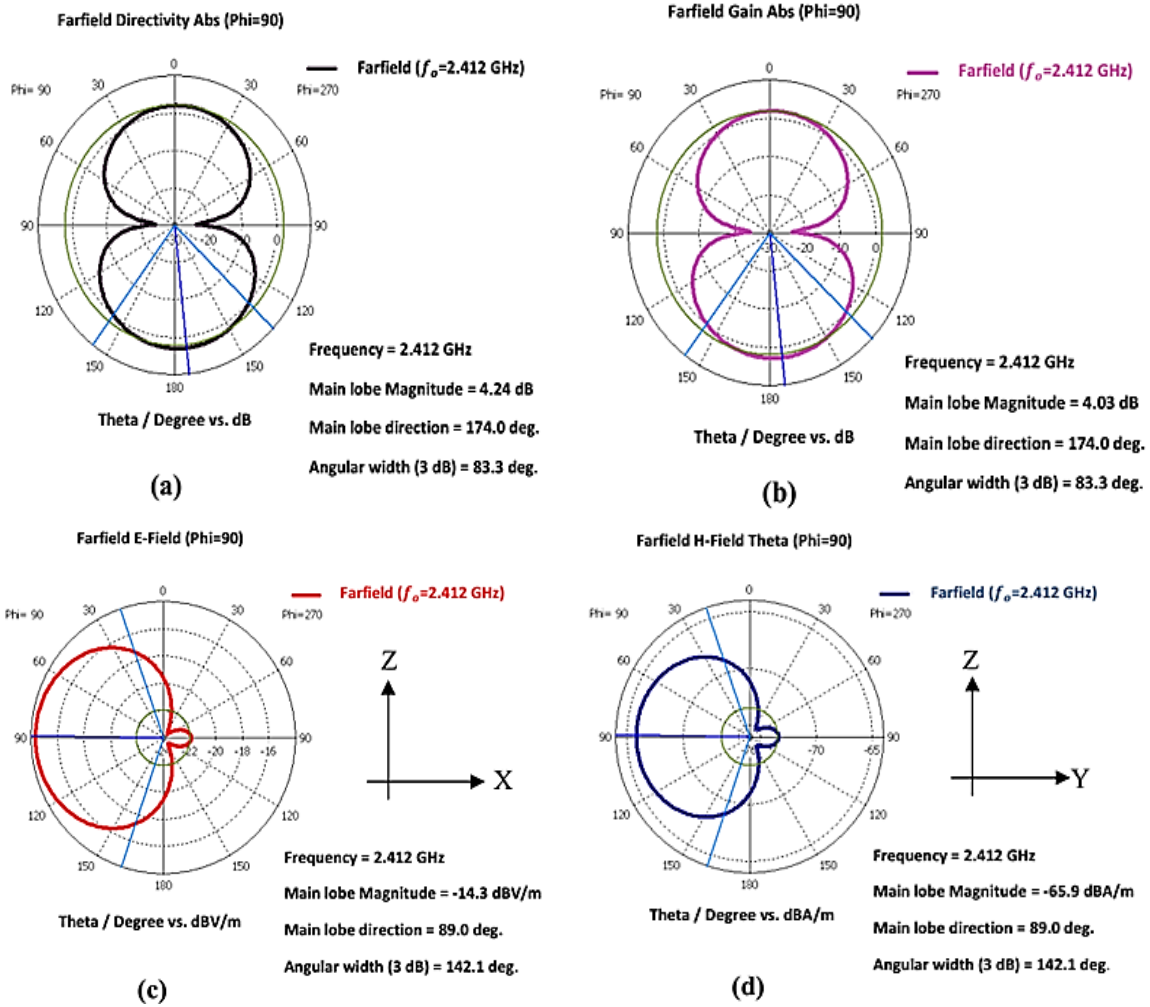| Parameters	Dielectric Substrate type		
	RT/ 5880	RO3003	FR-4
Center frequency (f_o GHz)	2.412	2.3	2.049
Return loss RL (dB)	-15	-13.063	-7.04
Maximum Gain (dB)	4.03	2.63	2.22
Substrate height (h)mils	30.944	50	63
BW (GHz)	1.22	1.3	1.059
VSWR	1.1937	1.52	2.24

Figure 9. Far-field radiation pattern of the filtering antenna: (a) Directivity, (b) Gain, (c) E-Field, (d) H-Field
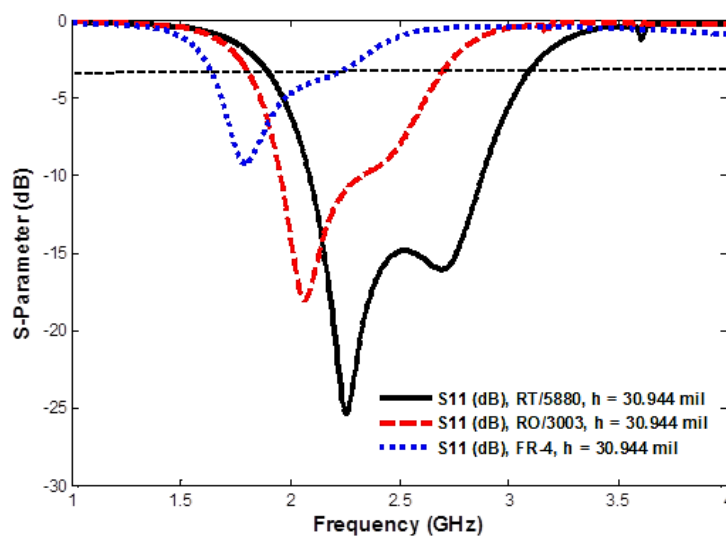

Figure 10. Comparison of S_{11}-parameter for the different dielectric substrate materials,
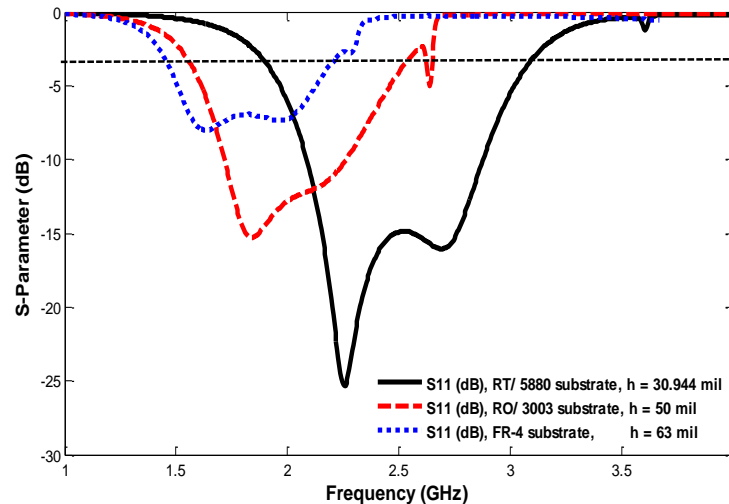h = 30.944 mil

Figure 11. Comparison of S_{11}-parameter for the different dielectric substrate materials
and different dielectric substrate heights

5. CONCLUSION

This article presents a microstrip filtering antenna design for WLAN applications in a center frequency $f_0 = 2.412$ GHz. A microstrip-filtering antenna has designed and built on a three different dielectric substrate materials, which are RT/Duroid 5880, RO3003, and FR-4. These designs have done by using CST microwave studio suite software. The filtering antenna consists of four-pole bandpass filter integrated to monopole patch antenna. The comparison of the filtering antenna design, which is using the three different dielectric substrate materials at a fixed substrate height and different heights have done. The results obtained from each design indicate that the most suitable design for this application when established on RT/Duroid 5880 dielectric substrate material.

REFERENCES

[1]   H. Liu, et al., "High-Temperature Superconducting Bandpass Filter Using Asymmetric Stepped-Impedance Resonators with Wide-Stopband Performance," *IEEE Transaction Applied Superconductivity,* vol. 25, no. 5, Oct 2015.
[2]   Y. Tu, et al., "An improved 860–960MHz fully integrated CMOS power amplifier designation for UHF RFID transmitter," *International Journal of Electronics and Communications,* vol. 67, no. 7, pp. 574-577, Jul 2013.
[3]   Y. I. Abdulraheem, et al., "Design of Frequency-reconfigurable Multiband Compact Antenna using two PIN diodes for WLAN/WiMAX Applications," *IET Microwaves, Antennas and Propagation,* vol. 11, no. 8, pp. 1098-1105, Jun 2017.
[4]   Y. Al-Yasir, et al., "Design of Very Compact Combline Band-Pass Filter for 5G Applications," *Loughborough Antennas & Propagation Conference, Loughborough, UK*, Nov 2018, pp. 1-4.
[5]   Y. I. A. Al-Yasir, et al., "Mixed-coupling multi-function quint-wideband asymmetric stepped impedance resonator filter," *Microwave and Optical Technology Letters*, vol. 61, no. 5, pp. 1181-1184, Jan 2019.
[6]   Y. I. A. Al-Yasir, et al., "New Multi-standard Dual-Wideband and Quad-Wideband Asymmetric Step Impedance Resonator Filters with Wide Stop Band Restriction," *International Journal of RF and Microwave Computer-Aided Engineering,* vol. 29, no. 8, Apr 2019.
[7]   W. Chen, et al., "Compact and Wide Upper-Stopband Triple-Mode Broadband Microstrip BPF," *TELKOMNIKA Telecommunication Computing Electronics and Control,* vol. 10, no. 2, pp. 353-358, Jun 2012.
[8]   E. S. Ahmed, "Compact Dual-Band Parallel Coupled T-Shaped SIR Filter for WLAN Applications," *TELKOMNIKA Telecommunication Computing Electronics and Control,* vol. 15, no. 4, pp. 1677-1681, Dec 2017.
[9]   M. Mabrok, et al., "Switchable Dual-band Bandpass Filter Based on Stepped Impedance Resonator with U-Shaped Defected Microstrip Structure for Wireless Applications," *TELKOMNIKA Telecommunication Computing Electronics and Control,* vol. 17, no. 2, pp. 1032-1039, Apr 2019.

[10] Y. I. A. Al-Yasir, et al., "Design of multi-standard single/tri/quint-wideband asymmetric stepped-impedance resonator filters with adjustable TZs," *IET Microwaves, Antennas & Propagation,* vol. 13, no. 10, pp. 1637-1645, Aug 2019.

[11] A. Taybi, et al., "A New Configuration of a High Output Voltage 2.45 GHz Rectifier for Wireless Power Transmission Applications," *TELKOMNIKA Telecommunication Computing Electronics and Control,* vol. 16, no. 5, pp. 1939-1946, Oct 2018.

[12] Y. I. A. Al-Yasir, et al., "A new polarization-reconfigurable antenna for 5G applications," *Electronics,* vol. 7, no. 11, pp. 1-9, Nov 2018.

[13] M. H. M. Salleh, et al., "The Investigation of Substrate's Dielectric Properties for Improving the Performance of Witricity Devices," *Applied Computational Electromagnetics Society Journal,* vol. 32, no. 1, pp. 24-30, Jan 2017.

[14] B. Yu, et al., "A novel 28 GHz beam steering array for 5G mobile device with metallic casing application," *IEEE Transactions on Antennas and Propagation,* vol. 66, no. 1, pp. 462-466, Jan 2018.

[15] Y. I. A. Al-Yasir, et al., "Recent Progress in the Design of 4G/5G Reconfigurable Filters," *Electronics,* vol. 8, no. 1, pp. 1-17, Jan 2019.

[16] A. Khan and R. Nema, "Analysis of five different dielectric substrates on microstrip patch antenna," *International journal of computer applications,* vol. 55, no. 18, pp. 6-12, Oct 2012.

[17] C. T. Chuang and S. J. Chung, "A new compact filtering antenna using defected ground resonator," *Microwave Conference Proceedings (APMC), 2010 Asia-Pacific*, pp. 1003-1006, 2010.

[18] J. Coonrod, "Choosing Circuit Materials for Millimeter Wave Applications," *High Frequency Electronics,* vol. 12, no. 7, pp. 22-30, Jul 2013.

[19] D. Leys, "Best materials for 3–6 GHz design," *Printed Circuit Design & Manufacture,* vol. 21, pp. 34-39, Nov 2004.

[20] A. Khare, et al., "New multiband E-shape microstrip patch antenna on RT DUROID 5880 substrate and RO4003 substrate for pervasive wireless communication," *International Jounal of Computer Applications,* vol. 9, no. 8, pp. 6-14, 2010.

[21] S. S. Afrin and P. R. Dev., "Performance Analysis of Microstrip Patch Antenna for Ultra Wide Band Application," Thesis, B.Sc. in Electronics & Telecommunication Engineering, East West University, 2015.

[22] K. Szostak and P. Słobodzian, "Broadband Dielectric Measurement of PCB and Substrate Materials by Means of a Microstrip Line of Adjustable Width," *IEEE Microwave and Wireless Components Letters*, vol. 28, no. 10, pp. 945-947, Oct 2018.

[23] K. Y. Kapusuz, et al., "Substrate-Independent Microwave Components in Substrate Integrated Waveguide Technology for High-Performance Smart Surfaces," *IEEE Transactions on Microwave Theory and Techniques*, vol. 66, no. 6, pp. 3036-3047, Jun 2018.

[24] G. Breed, "An introduction to defected ground structures in microstrip circuits," *High Frequency Electronics,* vol. 7, pp. 50-54, Nov 2008.

[25] F. S. Mahmud, et al., "Parametric studies on effects of defected ground structure (DGS) for dual band bandstop microstrip filter," *EPJ Web of Conferences*, vol. 162, 2017.